

L Number	Hits	Search Text	DB	Time stamp
1	2	6563751.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:22
2	2	6122762.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:23
3	1939	"I21" and (interface or control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:24
4	1	6070252.pn.	USPAT; US-PGPUB; IBM_TDB	2004/08/19 15:26
5	1	4969148.pn.	USPAT; US-PGPUB; IBM_TDB	2004/08/19 15:28
6	1	5355369.pn.	USPAT; US-PGPUB; IBM_TDB	2004/08/19 15:28
-	94450	(BIST) or (self adj test) or ((memory or logic) adj unit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 12:52
-	4586	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:11
-	4278	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:12
-	3780	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:12
-	1735	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:12
-	1736	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:13
-	1268	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:14
-	603	((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and test.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:14

-	9	(((((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and daisy adj chain\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:14
-	4	(((((BIST) or (self adj test) or ((memory or logic) adj unit)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and test.ab.) and (multi adj port).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:08
-	96056	(BIST) or (self adj test) or ((memory or logic) adj unit or jtag)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:59
-	4951	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:11
-	4640	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:00
-	4083	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:00
-	1842	(((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:53
-	1366	(((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:14
-	648	(((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and test.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:24
-	14	(((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and test.ab.) and daisy adj chain\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:02
-	16	(((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and 714/\$.ccls.) and (interface or control\$3)) AND @AD<20010329) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE).AB.) AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test\$3) and test.ab.) and(multi adj port)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:32
-	76219	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and (interface or control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:51

-	62596	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag)) and (interface or control\$3)) AND @AD<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:52
-	50728	"122" AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:53
-	1472	("122" AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:53
-	37	((("122" AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test.ab.) and daisy adj chain\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:54
-	23	((("122" AND (MEMORY OR RAM OR DRAM OR CAM OR ROM OR CORE or multi adj port).AB.) and test.ab.) and daisy adj chain\$3) and(not 714/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:55
-	257528	(BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 08:59
-	169310	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)) and (interface or control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:00
-	214210	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)) AND @AD<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:01
-	9106	((BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)) AND @AD<20010329) and test.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:01
-	106	((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)) AND @AD<20010329) and test.ab.) and daisy adj chain\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:02
-	42	((((BIST) or (self adj test) or ((memory or logic) adj unit or jtag or tap)) AND @AD<20010329) and test.ab.) and(multi adj port)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:47
-	0	preload adj write adj command	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:47
-	3	preload adj write	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:50

-	4958	memory adj test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:51
-	219	(memory adj test) and (read adj command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:51
-	135	((memory adj test) and (read adj command)) and (write adj command)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 09:52
-	109	((memory adj test) and (read adj command)) and (write adj command)) and @ad<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/09 10:34
-	8	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and 1149.1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:38
-	493	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:04
-	322	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))) and(memory aand test\$3).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:08
-	296	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))) and(memory aand test\$3).ab.) and @ad<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:10
-	90	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))) and(memory aand test\$3).ab.) and @ad<20010329) and mult\$4.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 14:58
-	179	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))) and(memory aand test\$3).ab.) and @ad<20010329) and multiplex\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:11
-	160	((multi adj port) or (two adj port) or (three adj port)) adj (memory or core or dram or sram or ram or rom or cam) and (test\$3 and (not 1149.1))) and(memory aand test\$3).ab.) and @ad<20010329) and multiplex\$3) and(data with address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/12/10 08:13
-	0	09/823,148	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 13:07
-	14	("6070252" or "5355369" or "6122762" or "4969148" or "6404684" or "5254942" or "6070256").pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 13:21

-	3324	MBIST or ABIST or (((BIST or (self adj test))with memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 13:23
-	130	((MBIST or ABIST or (((BIST or (self adj test))with memory))) and shadow) and @ad<20010329	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 13:24
-	185	(MBIST or ABIST or (((BIST or (self adj test))with memory))) and shadow	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 13:25